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THE UNITED STATES PATENT AND TRADEMARK OFFICE  
Case No. 13451US02

PATENT

In the Application of: )  
ESIN TERZIOGLU et al. )  
Serial No.: 09/775,476 )  
Filed: February 2, 2001 )  
For: ASYNCHRONOUSLY- )  
RESETTABLE DECODER WITH )  
REDUNDANCY )  
Examiner: Trong Q. Phan )  
Group Art Unit: 2818 )  
Confirmation No.: 7751 )

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence and all correspondence referenced herein are being deposited with the United States Postal Service as first class mail in an envelope addressed to the Asst. Commissioner for Patents, Washington, D.C. 20231 on December 16, 2002.

By: Ronald E. Larson  
Ronald E. Larson  
Reg. No. 24,478

**AMENDMENT B**

Assistant Commissioner for Patents  
Washington, DC 20231

Dear Sir:

In response to the Office Action dated September 16, 2002, kindly amend the above-identified application as follows:

In The Specification:

Kindly amend the specification as follows:

Kindly delete the paragraph beginning "FIG. 6" at page 26, line 24 and ending "or both" at page 27, line 12, and replace it with the following paragraph:

FIG. 6 illustrates a hierarchical structure 600 having hierarchical word-line decoding in which each hierarchical memory module 605 is composed of a predefined number of memory cells 610, which are coupled with a particular local word line decoder 615a-c. Each local word line decoder 615a-c is coupled with a respective

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